Three-Level Space Vector PWM in Low Index Modulation Region Avoiding Narrow Pulse Problem

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Abstract—In using GTO, a few hundreds micro seconds longer than specified pulse width must be guaranteed for safety for each device to commute with its inherent gate current sequences. This paper describes the mechanism of generating undesirable narrow pulses in conventional three-level space vector PWM and suggests two new algorithms of space vector PWM using Non-Nearest three and four vectors in a control period to avoid such a narrow pulse problem. Experimental verifications for the suggested algorithms are also presented.

I. INTRODUCTION

The three-level inverter topology is being widely used in high voltage/high power applications due to its high voltage handling and good harmonic rejection capabilities with currently available power devices like GTO's. Fig. 1 shows the basic circuit diagram of three-level inverter excluding detailed snubber circuits and antiparallel diodes. It is known that the three-level inverter roughly improves by a factor of four the harmonics content compared with conventional two-level topology having the same number of devices and ratings.

So far various PWM techniques controlling three-level inverter have been studied and a good plenty of results are published such as modified two-level triangular carrier modulation, cost function minimizing PWM and space vector PWM [1]-[3].

Among these, the space vector PWM is often preferred not only in the low modulation index region but also in the middle and high modulation index regions. The reason is due to its simplicity both in hardware and software and innate real time PWM capability which is essential for some applications such as field oriented induction motor control.

Especially the space vector PWM exhibits relatively good performance in the low modulation index region which we are concerned within this paper compared with the other PWM methods. The main superiorities are

1) coping well with frequently varying fundamental to switching frequency ratio,
2) simple hardware and software compared with other programmed strategies which are complex due to the vast amount of angle data,
3) asynchronous PWM is possible which is useful in the low frequency region.

Conventional space vector PWM strategy with NTV selection law using the closest three vectors to command, however, is not adequate in the low modulation index region because it generates narrow gating pulses. There are several important factors which must be considered in designing PWM method: i) available maximum device switching frequency, ii) harmonics content such as Total Harmonic Distortion (THD), iii) allowed Minimum Pulse Width (MPW), and iv) operating range. Among these, minimum pulse width is especially one of the most critical factors in GTO inverter because the on- and off-pulse widths must exceed at least a hundred microseconds in order for the GTO to commute with special gate current sequences.

The minimum pulse width becomes problematic as inverter frequency closes to either minimum or maximum. This is because dwell time at nonzero and zero vector becomes too narrow when the inverter frequency reaches either boundary value when constant voltage/frequency control is used.

The narrow pulse width problem has a close relation to starting performance. Solving this problem by ignoring pulse widths under a specified limit results in severe distorted output at low frequency, which means improper flux build up in the motor, resulting in low starting torque. Raising the starting frequency to avoid narrow pulses may cause large surge current hazardous to switches. So the method that does not generate narrow pulses at low frequency is desired.

In this paper the detailed mechanism of generation of narrow gating pulses in conventional NTV space vector PWM is described and new Non-Nearest Three and Four Vectors (N2TV and N'FV) selection laws are suggested to avoid such narrow pulse problems. The experimental results are also presented.

II. NARROW PULSE PROBLEM IN CONVENTIONAL SPACE VECTOR PWM WITH NEAREST THREE VECTOR (NTV) SELECTION LAW

Fig. 2 shows all space vectors of three-level inverter represented on d-q domain normalized by $\frac{2\sqrt{3}}{3}V_{DC}$. It is obtained by $d-q$ transforming the output voltage of inverter made by various switch combinations. +, 0, and − represent the

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Fig. 2. Space vectors of three-level inverter.

possible output terminal voltages of the inverter shown in Fig. 1, respectively, $+V_{dc}/2$, 0, and $-V_{dc}/2$ if $G$ is the reference. The space vector PWM is a PWM method that makes pseudo-flux have circular trajectory as far as possible, where the pseudo-flux is defined by

$$\psi = \int V dt.$$  \hspace{1cm} (1)

From now on, let us confine our discussion to PWM method only in the low modulation index less than 0.5(pu). The output frequency is also low when constant frequency/voltage ratio operation is used. In this case the modulation index, $m$, is defined by

$$m(pu) = \frac{|V_o|}{2/3 \cdot V_{dc}}$$ \hspace{1cm} (2)

where $V_o$: output voltage vector $V_{dc}$: dc-link voltage. Fig. 3(a) shows the inner part of Fig. 2 explaining the algorithm of conventional NTV space vector PWM at certain instant. The voltage command $V^*$ is rotating vector with magnitude of $m$ and angular velocity of $\omega$ given by

$$V^* = m \cdot \text{exp}(j\omega t) = m \cdot \text{exp}(j\theta).$$ \hspace{1cm} (3)

Thus the $V^*$ can fall in any triangle of Fig. 2 if the constant voltage/frequency method is used. Fig. 3(a) is a situation when $V^*$ falls in a triangle made by $V_0$, $V_1$, and $V_2$ which are voltage vector having magnitudes of 0, 0.5, and 0.5,

respectively. The command vector $V^*$ is approximated for time interval $T$ with following equations:

$$V^* = k_1 \cdot V_1 + k_2 \cdot V_2$$  \hspace{1cm} (4)

$$1 = k_0 + k_1 + k_2$$ \hspace{1cm} (5)

$$[t_0, t_1, t_2] = T \cdot [k_0, k_1, k_2]$$ \hspace{1cm} (6)

where $k_0$, $k_1$, and $k_2$ are the duty ratios of $V_0$, $V_1$, and $V_2$, respectively, and $t_0$, $t_1$, $t_2$ are the corresponding dwell times for the period $T$. In this case, the vector sequence is $V_0[000]-V_1[0-0]-V_2[+00]$ for natural point voltage control [3] and the pseudo-flux trajectory between two arbitrary points is shown in Fig. 3(b). It seems nice because the pseudo-flux trajectory is much closed to the straight line passing the two points but there exist unallowed narrow pulses. Rewriting (4),

$$\begin{bmatrix} V_{d*} \\ V_{q*} \end{bmatrix} = \begin{bmatrix} V_{d1} \\ V_{q1} \end{bmatrix} k_1 \begin{bmatrix} V_{d2} \\ V_{q2} \end{bmatrix} k_2$$ \hspace{1cm} (7)

$$\begin{bmatrix} m \cdot \cos \theta \\ m \cdot \sin \theta \end{bmatrix} = \begin{bmatrix} 0.5 & 0.5 \cdot \cos 60^\circ \\ 0 & 0.5 \cdot \sin 60^\circ \end{bmatrix} \begin{bmatrix} k_1 \\ k_2 \end{bmatrix}, 0^\circ \leq \theta < 60^\circ.$$ \hspace{1cm} (8)

Therefore,

$$\begin{bmatrix} \frac{k_1}{m} \\ \frac{k_2}{m} \end{bmatrix} = \begin{bmatrix} 0.5 & 0.5 \cdot \cos 60^\circ \\ 0 & 0.5 \cdot \sin 60^\circ \end{bmatrix}^{-1} \begin{bmatrix} \cos \theta \\ \sin \theta \end{bmatrix}, 0^\circ \leq \theta < 60^\circ.$$ \hspace{1cm} (9)

Fig. 4 shows $k_1/m$ and $k_2/m$ obtained from (9) for $0^\circ \leq \theta < 60^\circ$. This is repeated every $60^\circ$ as $\theta$ increases. $k_1/m$ and $k_2/m$ approach zero when $\theta$ becomes close to $0^\circ$ and $60^\circ$, respectively. If $m = 0.01$ then, about one half of all $\theta$'s belong to the region where the dwell time is less than 100 $\mu$s when $T = 8 \mu$s, and quarter of $\theta$'s belong to this region when $T = 16 \mu$s. In order to maintain the minimum pulse width longer than a specified time interval, $T$ must be taken as large as possible and the dwell time below that interval should be ignored. However, this approach forces under utilization of maximum available switching frequency of the devices and increases flux and torque ripples significantly.
III. SUGGESTED SPACE VECTOR PWM: NON-NEAREST THREE-VECTOR (N²TV) SELECTION LAW

Very sharp pulses are generated using the NTV law in the low modulation index region because the nearest vectors are selected to approximate \( V^* \). If some vectors far from the command vector \( V^* \) are selected, their duty ratio is longer. The authors suggest a space new vector PWM using an N²TV selection law which uses the voltage vectors \( V_1 \) and \( V_3 \) further away from the command vector \( V^* \) than the \( V_1 \) and \( V_2 \) used in NTV, as shown in Fig. 5(a). In this case the command vector \( V^* \) in region A can be approximated by the following relations like in NTV:

\[
V^* = k_1 \cdot V_1 + k_3 \cdot V_3
\]

\[
1 = k_0 + k_1 + k_3
\]

\[
[t_0, t_1, t_3] = T \cdot [k_0, k_1, k_3].
\]

Rewriting (10),

\[
\begin{bmatrix}
V^*_2 \\
V^*_3
\end{bmatrix} = \begin{bmatrix}
V_{21} & V_{23} \\
V_{31} & V_{33}
\end{bmatrix} \begin{bmatrix}
k_1 \\
k_3
\end{bmatrix}
\]

\[
\begin{bmatrix}
k_0 \\
k_1 \\
k_3
\end{bmatrix} = \begin{bmatrix}
0.5 \cdot \cos(-30^\circ) & 0.5 \cdot \sin(-30^\circ) \\
0.5 \cdot \cos(30^\circ) & 0.5 \cdot \sin(30^\circ)
\end{bmatrix}^{-1} \begin{bmatrix}
k_1 \\
k_3
\end{bmatrix}, 0^\circ \leq \theta < 60^\circ.
\]

IV. MODIFIED N²TV METHOD: NON-NEAREST FOUR-VECTOR (N²FV) SELECTION LAW

With the N²TV method described previously, only the \( V^* \) in region A [in Fig. 7(a)] can be approximated. This is understandable considering that the center of mass of plate A cannot be placed outside region A. So, additional \( V_2 \) vector
Fig. 9. Pseudo flux trajectories when \( f_0 = 2.5 \text{ Hz}, m = 0.05, \) and \( T = 6.67 \text{ ms} \), by (a) Nearest Three-Vector selection law allowing narrow pulses, (b) Nearest Three-Vector selection law ignoring narrow pulses under 100 ps, (c) Non-Nearest Three-Vector selection law, (d) Non-Nearest Four-Vector selection law.

must be included between \( V_1 \) and \( V_3 \). We call this method the Non-Nearest Four-Vector (N\( ^2 \)FV) selection law covering both \( A \) and \( B \) region. The pseudo-flux trajectory is shown in Fig. 7(b) in period \( T \). We can see without proof that the flux ripple is somewhat reduced compared to N\( ^n \)TV [Fig. 5(b)] by introducing one more vector close to \( V^* \). Now we have the following equations relating the four vectors:

\[
V^* = k_1 \cdot V_1 + k_2 \cdot V_2 + k_3 \cdot V_3
\]

\[
k_1 = k_0 + k_1 + k_2 + k_3
\]

\[
[t_0, t_1, t_2, t_3] = T \cdot [k_0, k_1, k_2, k_3]
\]

There exist many solutions satisfying the above equations because there are four unknown variables and only three equations [two in (17) and one in (18)]. On the other hand, considering that \( k_2 \) should be taken as large as possible in order to reduce the output harmonics satisfying \( \min(t_1, t_3) \geq MPW \), then, \( k_2 \) is defined by

\[
k_2/m = \min(k_1/m, k_3/m) - (k/m)_{\theta=0^\circ,60^\circ}
\]

\[
= \min(k_1/m, k_3/m) - 1.155
\]

where,

\[
\min(A, B) = \begin{cases} A, & \text{if } A \geq B \\ B, & \text{if } A < B \end{cases}
\]

With the relation of

\[
V_2 = V_1 + V_3,
\]

the modified duty ratio \( k_1/m \) and \( \tilde{k}_3/m \) are obtained as

\[
k_1/m = k_1/m - k_2/m
\]

\[
k_3/m = k_3/m - k_2/m.
\]

Now, we have

\[
V^* = \tilde{k}_1 \cdot V_1 + k_2 \cdot V_2 + \tilde{k}_3 \cdot V_3
\]

\[
1 = k_0 + \tilde{k}_1 + k_2 + \tilde{k}_3
\]

\[
[t_0, t_1, t_2, t_3] = T \cdot [k_0, \tilde{k}_1, k_2, \tilde{k}_3]
\]

where \( \tilde{k}_1/m, k_2/m, \) and \( \tilde{k}_3/m \) for \( 0^\circ \leq \theta < 60^\circ \) are shown in Fig. 8. Because \( V_2 \) is inserted in a way that \( V_0[000] - V_1[+0+] - V_2[+00] - V_3[+ +0] \), even small \( k_2 \) is not problematic. It is worth noting that N\( ^2 \)FV does not increase the switching frequency compared with N\( ^n \)TV by introducing \( V_2 \).

V. EXPERIMENTAL RESULTS

To verify the validity of the proposed N\( ^2 \)TV and N\( ^2 \)FV algorithm, we tested these methods with real time three-level simulated inverter and 10 KVA transistor inverter being controlled by Motorola digital signal processor, DSP56000. The simulated inverter consists of several OP amps and offers various quantities of three-level inverter such as pole voltages \( (V_{AG}, V_{BG}, V_{CG}) \), where, \( G \) is same as in Fig. 1), line to line voltages \( (V_{AB}, V_{BC}, V_{CA}) \), \( d-q \) component of output voltages \( (V_{d}, V_{q}) \), and pseudo-flux trajectories \( (\Psi_\phi, \Psi_q) \). We can see space vector of Fig. 2 and pseudo-flux trajectories using oscilloscope with X-Y display mode. The simulated inverter serves as very useful tool by preventing power circuit from being destroyed by erroneous control operation during debug process. The controller debugged by simulated inverter may then be applied to real power system.

Figs. 9 and 10 are experimental results with output frequency \( f_o = 2.5 \text{ Hz}, m = 0.05, \) and \( T = 6.67 \text{ ms} \). Figs. 9(a) and (b) are pseudo-flux trajectories by NTV algorithm: (a) is obtained when allowing even narrow pulses.
and cannot be used in real situation whereas (b) is obtained when pulses having width under 100 µs are ignored, showing much distortion. Fig. 9(c) is that of N²TV and shows somewhat large excursion around the command but has no narrow pulses under 100 µs. Whereas, Fig. 9(d) is obtained by N²FV and shows much reduced excursion compared with N²TV without generating narrow pulses. Fig. 10 displays the oscillodiagrams of the line voltage which is one of $V_{AG}$, $V_{BG}$, and $V_{CG}$ (upper trace) and its magnification (lower trace) along the time axis in order to compare minimum pulse width of two algorithms. The voltage waveforms of Fig. 10(b) have some third harmonic component, but balanced load in three-phase system cannot see this. The minimum pulse width was 64 µs in NTV whereas 444 µs in N²FV.

In the higher modulation index region, the programmed synchronous PWM strategies such as harmonic elimination and optimal PWM are recommended because of their good harmonic quality and efficiency. At transition from one strategy to another, there must be no discontinuity both in magnitude of fundamental component and phase angle so as to reduce abnormal current spikes. The transition must occur when $\theta = 0^\circ, 30^\circ, 60^\circ, \ldots, 270^\circ$ in order not to violate symmetry of synchronous PWM. Fig. 11 shows one transition from asynchronous N²FV to synchronous harmonic elimination PWM when $\theta = 90^\circ$. Once transition is determined, the asynchronous N²FV continues until $\theta$ is in possible transition region shown in the figure. At turning period, $\theta$ is jumped to 90º position with new time period $T_t = \frac{\Delta \theta}{\Delta \theta}$. This method makes no problem because $T_t$ is in the range of $T \leq T_t < 2T$. After this, the harmonic elimination pattern follows subsequently. Experimental result of Fig. 12 shows the smooth transition with arrow indicating transition point.

VI. CONCLUSION

The detailed mechanisms of generating narrow pulses critical to GTO devices for conventional three-level space vector in the low modulation index region are explained. In this paper we suggested a new space vector PWM method guaranteeing minimum pulse width to overcome specified operation limit named as N²TV and N²FV selection law. The experimental results show that the N²FV has the narrow pulse rejection capability.

REFERENCES

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